Cambridge Assessment International Education
Cambridge International General Certificate of Secondary Education

COMPUTER SCIENCE **0478/12**

Paper 1 **October/November 2019**

MARK SCHEME

Maximum Mark: 75

Published

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the October/November 2019 series for most Cambridge IGCSE™, Cambridge International A and AS Level components and some Cambridge O Level components.

This syllabus is regulated for use in England, Wales and Northern Ireland as a Cambridge International Level 1/Level 2 Certificate.

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Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

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GENERIC MARKING PRINCIPLE 5: Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).
GENERIC MARKING PRINCIPLE 6: Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

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<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>1</td><td>One mark for each correct tick

<table>
<tr><th>Statement</th><th>True (✓)</th><th>False (✓)</th></tr>
<tr><td>25 kB is larger than 100 MB</td><td></td><td>✓</td></tr>
<tr><td>999 MB is larger than 50 GB</td><td></td><td>✓</td></tr>
<tr><td>3500 kB is smaller than 2 GB</td><td>✓</td><td></td></tr>
<tr><td>2350 bytes is smaller than 2 kB</td><td></td><td>✓</td></tr>
</table></td><td>4</td></tr>
</table>

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Question	Answer	Marks
2	**Four** from: • Arithmetic and logic unit (ALU) • Memory address register (MAR) • Memory data register (MDR) // Memory buffer register (MBR) • Accumulator (ACC) • Immediate Access Store (IAS) • Main memory // RAM • Program counter (PC) • Current instruction register (CIR) • Address bus • Data bus • Control bus • Input device • Output device • Secondary storage device	**4**

Question	Answer	Marks
3(a)	**One** from: • Continuous data // by description • Non-discrete data // by description • By example, e.g. data such as a sound wave	**1**
3(b)	**One** from: • Discrete data that has only two values • By example, e.g. binary data / 1's and 0's	**1**

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Question	Answer	Marks
4(a)	• 52	**1**
4(b)	1 1 0 1 0 0 0 0	**1**
4(c)	• It is multiplied by 4	**1**

Question	Answer	Marks
5(a)	**Four** from: • A compression algorithm is used • Discards any unnecessary sounds … • … using perceptual musical shaping • … such as removing background noise / sounds humans can't hear // or other suitable example • Reduces sample size / resolution // by example • Reduces sample rate // by example • Sound is clipped • The data is permanently removed	**4**
5(b)(i)	**One** from: • The file size will be smaller than lossless • Requires less storage space • Requires less time to transmit	**1**
5(b)(ii)	**One** from: • The quality of the sound will be reduced • The original file cannot be restored	**1**

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Question	Answer	Marks
5(c)(i)	**Four** from: • Musical Instrument Digital Interface file • Stores a set of commands / instructions for how the sound should be played • Does not store the actual sounds • Data in the file has been recorded using digital instruments • Specifies pitch of the note // specifies the note to be played • Specifies when each note plays and stops playing // Specifies key on/off • Specifies duration of the note • Specifies volume of the note • Specifies the tempo • Specifies the type of instrument	**4**
5(c)(ii)	**Four** from: • It uses a single wire … • … therefore, it is cheaper to manufacture / buy / install • … therefore, less likely to have interference // no crosstalk • … therefore, can be used over longer distances • Data is sent a bit at a time … • … therefore, less chance of data being skewed // data is received in order • Transmission can be synchronised … • … can reduce rate of errors	**4**

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<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>6</td><td>One mark for each correct tick
<table>
<tr><th>Statement</th><th>Resistive (✓)</th><th>Capacitive (✓)</th></tr>
<tr><td>This touch screen has multi-touch capabilities</td><td></td><td>✓</td></tr>
<tr><td>This touch screen cannot be used whilst wearing gloves</td><td></td><td>✓</td></tr>
<tr><td>This touch screen is made up of two layers with a small space in between</td><td>✓</td><td></td></tr>
<tr><td>This touch screen uses the electrical properties of the human body</td><td></td><td>✓</td></tr>
<tr><td>This touch screen is normally cheaper to manufacture</td><td>✓</td><td></td></tr>
<tr><td>This touch screen has a quicker response time</td><td></td><td>✓</td></tr>
</table></td><td>6</td></tr>
</table>

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Question	Answer	Marks
7(a)	**Four** from: • Membrane / matrix / circuit board present at base of keys • A key is pressed that presses a switch • When a key is pressed it completes a circuit // changes the current in a circuit • The location of the keypress is calculated • An index of characters is searched to find the corresponding keypress • Each character has an ASCII / Unicode value • The ASCII / Unicode value has a binary value • Keypress generates an interrupt • Each character / keypress is added to a buffer to wait to be processed • The binary can then be processed by the CPU to action the key press	**4**
7(b)	**Three** from: • Display a web page • Sends a request to the web server • Receives data from web server • Translates HTML files • Processes client-side script, e.g. JavaScript • Store favourites • Store history • Navigation forward and backward • Check security • Store / access cookies • Find specific text within a web page • Downloading file from the web • Allows a homepage • Allows multiple tabs / web pages to be opened • Stores data in its cache	**3**

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Question	Answer	Marks
7(c)	**Three** from: • Hypertext Transfer Protocol Secure // It is a protocol … • … that is a set of rules/standards • Secure version of HTTP • Secure website // secures data • Uses TLS / SSL • Uses encryption	**3**

Question	Answer	Marks
8(a)	• X = 1 if (A is 1 XOR C is 1) OR (B is 1 NAND C is NOT 1) • X = (A XOR C) OR (B NAND NOTC) **One** mark for each bullet: • (A XOR C) • OR • (B NAND NOTC)	**3**

<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>8(b)</td><td>
Four marks for 8 correct outputs
Three marks for 6 or 7 correct outputs
Two marks for 4 or 5 correct outputs
One mark for 2 or 3 correct outputs

<table>
<tr><th>A</th><th>B</th><th>C</th><th>Working space</th><th>X</th></tr>
<tr><td>0</td><td>0</td><td>0</td><td></td><td>1</td></tr>
<tr><td>0</td><td>0</td><td>1</td><td></td><td>1</td></tr>
<tr><td>0</td><td>1</td><td>0</td><td></td><td>0</td></tr>
<tr><td>0</td><td>1</td><td>1</td><td></td><td>1</td></tr>
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</table>
</td><td>4</td></tr>
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<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>9(a)</td><td>One mark per each correct tick

<table>
<tr><th>Statement</th><th>True (✓)</th><th>False (✓)</th></tr>
<tr><td>Duplex data transmission can be either serial or parallel</td><td>✓</td><td></td></tr>
<tr><td>Duplex data transmission is when data is transmitted both ways, but only one way at a time</td><td></td><td>✓</td></tr>
<tr><td>Duplex data transmission is always used to connect a device to a computer</td><td></td><td>✓</td></tr>
<tr><td>Duplex data transmission is when data is transmitted both ways at the same time</td><td>✓</td><td></td></tr>
<tr><td>Duplex data transmission automatically detects any errors in data</td><td></td><td>✓</td></tr>
</table></td><td>5</td></tr>
<tr><td>9(b)</td><td>• Parallel data transmission</td><td>1</td></tr>
</table>

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Question	Answer	Marks
9(c)	**Four** from (maximum two marks per benefit): • It is a universal standard … • … so it is likely to be compatible with the computer • It can only be inserted one way … • … so there is less chance of connecting a device incorrectly • It is a high-speed connection … • … so data will be transmitted quicker • It uses serial transmission … • … so it is cheaper to manufacture/buy • … less chance of skewing / errors • It doesn't require a (wireless) network … • … therefore, can be used if a network is down • It is backwards compatible … • … so no additional technology is needed • It can power the device … • … therefore no separate source of power is needed • Drivers are automatically downloaded // device is automatically identified … • … so no need to find them online / install them manually	**4**

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Question	Answer	Marks
10(a)	**Four** from: • Validation method • Used to check data entry • Digit is calculated from data // by example • Digit is appended / added to data • Digit is recalculated when data has been input • Digits are compared • If digits are different, error is detected // If digits match, no error is detected	**4**
10(b)	**Six** from (maximum three marks per security method): • Firewall … • … Monitors the traffic • … **Blocks** any traffic that doesn't meet the **criteria** / **rules** • (Strong) password // biometric … • … Data cannot be accessed without the use of the password / bio data • … Prevent brute force attacks • Encryption … • … Data will be scrambled • … **Key** is required to decrypt the data • … If data is stolen it will be meaningless • Physical security methods … • … The physical security will need to be overcome • … This can help deter theft of the data • Antispyware … • … will remove any spyware from system • … will prevent data being relayed to a third party	**6**

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Question	Answer	Marks
11(a)	**RAM** • To store the data / instructions / parts of OS that are currently in use **ROM** • To store the firmware / bootup instructions / BIOS **SSD** • To store files / software // by example	**3**
11(b)	**Two** from: • It is more durable // it has no moving parts • It has a faster read / write / access speed • It is more compact / light weight / smaller / portable • It uses less energy // battery will last longer • It is quieter • Not affected by magnetic forces • It runs at a cooler temperature • Less latency // takes less time to warm up	**2**

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