Cambridge Assessment International Education

Cambridge International AS & A Level

COMPUTER SCIENCE **9618/33**

Paper 3 Advanced Theory **October/November 2023**

MARK SCHEME

Maximum Mark: 75

Published

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the October/November 2023 series for most Cambridge IGCSE, Cambridge International A and AS Level components, and some Cambridge O Level components.

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Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently, e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

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Question	Answer	Marks
1(a)	**One** mark for working (**Max 1**) • conversion of 65.25 to binary seen e.g. 1000001.01 = 65.25 // 64 + 1 + 0.25 / ¼ **One** mark per mark point (**Max 2**) • correct mantissa • correct exponent **Mantissa**: 0 1 0 0 0 0 0 1 0 1 0 0 **Exponent**: 0 1 1 1	**3**
1(b)	**One** mark per mark point (**Max 2**) MP1 the decimal fraction 0.20 cannot be represented **exactly** (the closest is 0.25 / 0.1875) MP2 therefore, there will be a loss of precision due to a rounding error/truncation	**2**

Question	Answer	Marks
2(a)	**One** mark for each correct line connecting a protocol to its most appropriate description (**Max 4**). **Protocol**: HTTP, BitTorrent, SMTP, IMAP **Use**: to provide peer-to-peer file sharing; when retrieving email messages from a mail server over a TCP/IP connection; when transmitting hypertext documents; to map MAC addresses onto IP addresses; when sending email messages towards the intended destination	**4**
2(b)	**One** mark per mark point (**Max 2**) MP1 To ensure correct network protocols are followed MP2 To enable the upper layers to access the **physical medium** // enables connection/ communication with the internet / network layer MP3 To be responsible for transporting data within the network/local segments MP4 To format the data into frames for transmission MP5 Maps IP addresses to MAC/Physical addresses.	**2**

Question	Answer	Marks
3	**One** mark per mark point – enumerated type (**Max 2**) MP1 A user-defined non-composite (data type) (only award once) MP2 …with a list of all possible values MP3 …that is ordered. **One** mark per mark point – pointer type (**Max 2**) MP4 A user-defined non-composite (data type) (only award once) MP5 …that stores addresses/memory locations only MP6 …and indicates the type of data stored in the memory location.	**4**

Question	Answer	Marks
4(a)	**One** mark per mark point – sequential (**Max 2**) MP1 **Records** (in the file) are ordered MP2 **…**based on the **key field** MP3 A new version (of the file) has to be created to update the file **One** mark per mark point – random (**Max 2**) MP4 **Records** are stored in no particular order within the file // There is no sequencing in the placement of the **records** MP5 There is a relationship between the key of the **record** and its location within the file // a hashing algorithm is used to find the location of the **record** MP6 Updates to the file can be carried out directly.	**4**
4(b)	**One** mark per mark point (**Max 2**) MP1 Start at the beginning of the file MP2 …check records linearly MP3 …until the desired record is found // … processing / updating records as required //… EOF found.	**2**

Question	Answer	Marks
5	**One** mark per mark point – SISD (**Max 2**) MP1 Single Instruction, Single Data (architecture). // Data is taken from a single source and a single instruction is performed on the data. MP2 Contains **one** processor, a control unit and a memory unit. MP3 …that executes instructions sequentially. **One** mark per mark point – MIMD (**Max 2**) MP4 Multiple Instruction, Multiple Data (architecture). // At any time, any processor can execute different instructions on different sets of data. MP5 Contains **many** processors MP6 …that operate **asynchronously / independently**.	**4**

<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>6(a)</td><td>One mark for every shaded block of rows for column Z correct (Max 3)

<table>
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</table></td><td>3</td></tr>
<tr><td>6(b)</td><td>One mark for correct working from points (Max 2), for example:
(Y =) $\overline{A}.\overline{B}.C.\overline{D} + \overline{A}.\overline{B}.C.D + A.\overline{B}.C.\overline{D} + A.B.C.\overline{D}$
(Y =) $\overline{A}.\overline{D}.(\overline{B}.\overline{C} + \overline{B}.C + B.\overline{C} + B.C)$
(Y =) $\overline{A}.\overline{D}.(\overline{B}.(\overline{C} + C) + B.(\overline{C} + C))$
(Y =) $\overline{A}.\overline{D}.(\overline{B}.(1) + B.(1))$
(Y =) $\overline{A}.\overline{D}.(\overline{B} + B)$
(Y =) $\overline{A}.\overline{D}.(1)$

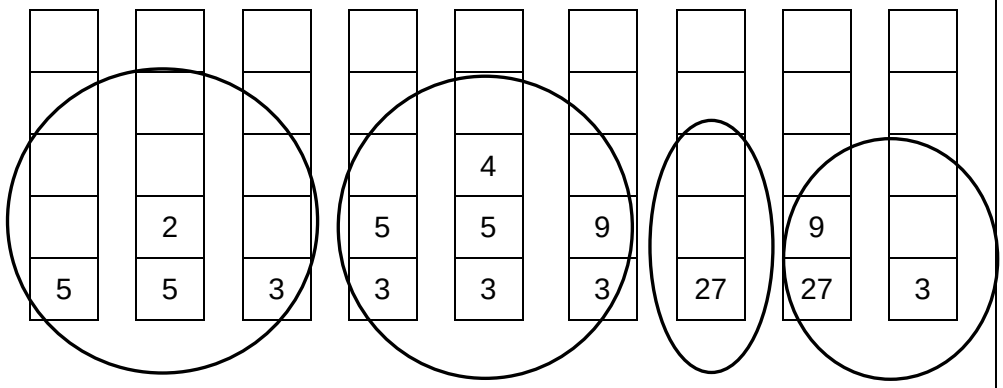
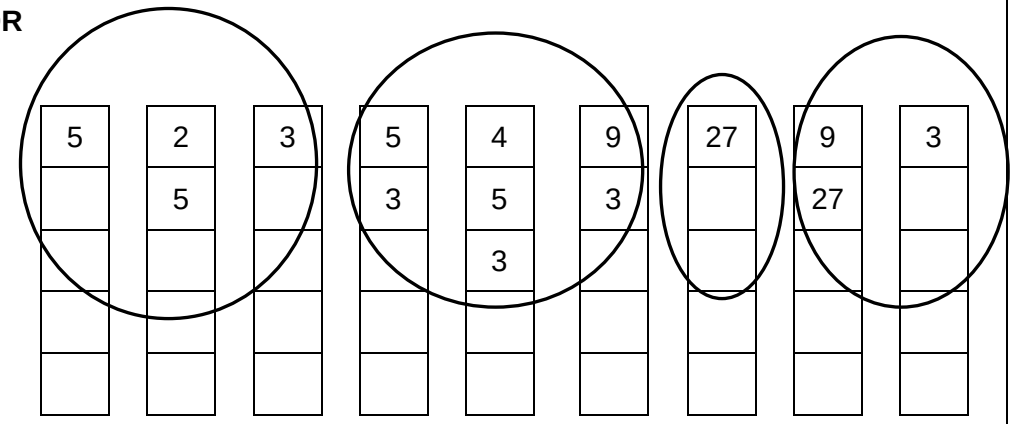
One mark for correct answer
(Y =) $\overline{A}.\overline{D}$</td><td>3</td></tr>
</table>

Question	Answer	Marks
7(a)	**One** mark for a benefit (**Max 1**) e.g. MP1 The user interface hides the complexities of the computer hardware/operating system from the user MP2 It provides appropriate access systems for users with differing needs MP3 Complex commands involving memory locations/buses/computer hardware/ are avoided **One** mark for a valid example (**Max 1**) e.g. Clicking on icon rather than writing code Using a graphical user interface / icons for navigation	**2**
7(b)	Blocked (state)	**1**
7(c)	**One** mark per mark point (**Max 2**) MP1 When the time slice of the running process expires (round robin). MP2 …and there is a process with a higher priority in the ready queue, the running process is pre-empted MP3 When an interrupt arrives at the CPU, (the process running on the CPU gets pre-empted).	**2**

Question	Answer	Marks

8(a)	**One** mark for each correctly completed line (**Max 5**)
	
	`DECLARE Customer : TAccount`
	`DECLARE Location : INTEGER`
	`DECLARE AccountFile : STRING`
	`AccountFile ← "AccountRecords.dat"`
	`OPENFILE AccountFile FOR RANDOM`
	`OUTPUT "Please enter an account number"`
	`INPUT Customer.AccountNumber`
	`Location ← Hash(Customer.AccountNumber)`
	`SEEK AccountFile, Location`
	`GETRECORD AccountFile, Customer`
	`OUTPUT Customer`
	`CLOSEFILE AccountFile`
		5

8(b)	**One** mark for correct definition (Exception handling is the process of) responding to an unexpected **event** when the **program is running** so it does **not halt unexpectedly**	**1**
8(c)	**One** mark per mark point (**Max 2**), for example: • Programming errors • User errors • Hardware failure • Runtime errors	**2**

Question	Answer	Marks
9(a)(i)	**One** mark per mark point (**Max 2**) • `(5 – 2)` • `* (5 + 4) / 9` Final correct expression `(5 – 2) * (5 + 4) / 9`	**2**
9(a)(ii)	**One** mark per ring (**Max 4**)  **OR**  	**4**
9(b)	**One** mark per mark point (**Max 3**) MP1 Evaluate the RPN expression from left to right MP2 Push each element of the RPN expression onto the stack in order until an operator is reached MP3 Pop the last two elements from the stack and apply the operator MP4 Push the result of the operation onto the stack MP5 Repeat the process until the whole expression is evaluated.	**3**

Question	Answer	Marks

10(a)	**One** mark per mark point (**Max 3**)
	MP1 Correct constant declaration
	MP2 Two correct variable declarations
	MP3 Correct array declaration
	
	Example answer:
	`CONSTANT Capacity = 25`
	`DECLARE BasePointer : INTEGER`
	`DECLARE TopPointer : INTEGER`
	`DECLARE Stack : ARRAY[1:25] OF REAL`
		3

10(b)	**One** mark for each correctly completed line (**Max 5**)
	
	`// popping an item from the stack`
	`FUNCTION Pop() RETURNS REAL`
	`  DECLARE Item : REAL`
	`  Item ← 0`
	`  IF TopPointer >= BasePointer THEN`
	`  Item ← Stack[TopPointer]`
	`  TopPointer ← TopPointer – 1`
	`  ELSE`
	`  OUTPUT "The stack is empty – error"`
	`  ENDIF`
	`  RETURN Item`
	`ENDFUNCTION`
		5

10(c)	**One** mark per mark point (**Max 2**) MP1 A queue is a first in first out / FIFO data structure and a stack is a first in last out / FILO / LIFO data structure // Data is removed from a queue in the order it is received and removed from a stack in the reverse order to which it is received MP2 Both ADTs can vary in size / are of indeterminate length MP3 Data is popped and pushed (onto/from a stack) at the same end but it is enqueued and dequeued (to/from a queue) at different/opposite ends // a queue has two accessible ends and a stack has only one MP4 A stack has only one moveable pointer whereas a queue has two.	**2**

Question	Answer	Marks

11(a)	**One** mark for each correctly completed clause (**Max 3**)
	
	`(22) student(anthony).`
	`(23) choice1(anthony, history).`
	`(24) choice2(anthony, geography).`
		3

11(b)	`X = tomaz, pietre, nico`	**1**

Question	Answer	Marks
11(c)	**One** mark per mark point (**Max 4**) • `student(N)` • `subject(S)` • `choice1(N, S)` • **all** logical operators correct with no additional code (see example answers) Example answers: `may_choose_subject(N, S)` `IF` **`student(N) AND subject(S) AND NOT choice1(N, S)`** `may_choose_subject(N, S)` `IF` **`NOT choice1(N, S), student(N), subject(S)`**	**4**

Question	Answer	Marks
12	**One** mark per mark point (**Max 4**) MP1 An artificial neural network is the component of artificial intelligence that is meant to simulate the functioning of a biological brain. MP2 Artificial neural networks are a key component of machine learning. MP3 They can solve problems that would prove impossible or difficult for humans // Artificial neural networks have self-learning capabilities that enable them to produce better results as more data becomes available MP4 Artificial neural networks can be layered (input, hidden and output layers) // Artificial neural networks have many interconnected layers, some / many of which are hidden MP5 Weights are assigned between nodes MP6 Weights are adjusted through training to give a more accurate result MP7 More complex learning capabilities / more accurate results are available with larger numbers of hidden layers	**4**

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