Cambridge International AS & A Level

**Cambridge Assessment International Education**
Cambridge International Advanced Subsidiary and Advanced Level

**COMPUTER SCIENCE** **9608/33**

Paper 3 Written Paper **October/November 2019**

MARK SCHEME

Maximum Mark: 75

**Published**

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the October/November 2019 series for most Cambridge IGCSE™, Cambridge International A and AS Level components and some Cambridge O Level components.



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## Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

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| Question | Answer | Marks |
|---|---|---|
| 1(a)(i) | **1 mark** per bullet point<br>• Exponent 0010 = 2<br>• Mantissa 0.1010010 becomes 010.10010 // $\frac{41}{64}$ // $2+\frac{1}{2}+\frac{1}{16}$<br>• Answer $2\frac{9}{16}$ // 2.5625 | **3** |
| 1(a)(ii) | **1 mark** per bullet point<br>• $-3.75$ = 100.01000 // $-4+\frac{1}{4}$ / 0.25<br>• 100.01000 becomes 1.0001000 Exponent = $+2$<br>• Answer: Mantissa = 10001000 Exponent = 0010 | **3** |
| 1(b) | Only the range is increased (no effect on precision) | **1** |
| 1(c) | **1 mark** per bullet point to **max 1**<br>• There is no **exact** binary conversion for some numbers<br>• More bits are needed to store the number than are available | **1** |
| 1(d) | First term: Overflow<br>Second term: Underflow | **2** |

| Question | Answer | Marks |
|---|---|---|
| 2(a)(i) | `35` is not a variable | **1** |
| 2(a)(ii) | `:=` is not an operator | **1** |
| 2(a)(iii) | `9` is not a digit | **1** |
| 2(b) | **1 mark** for each bullet point<br><br>`<operator>::=`<br>• `==` **`\|`** `>` **`\|`** `<`<br><br>`<number>::=`<br>• `<digit><digit>`<br><br>`<variable>::=`<br>• `<letter>`<br>• **`\|`**`<letter><variable>`<br><br>`<condition>::=`<br>• `<variable><operator><number>`<br>• **`\|`**`<variable><operator><variable>` | **6** |

| Question | Answer | Marks |
|---|---|---|
| 3(a) | **1 mark** per bullet point to **max 2**<br><br>• Provide a set of standards for transmission of data<br>• … that gives a known/accepted set of rules for transmitting and receiving data<br>• This enables communication/compatibility between devices from different manufacturers/platforms etc. | **2** |
| 3(b) | **1 mark** per bullet point to **max 3**<br><br>• Carrier Sense Multiple Access (with) Collision Detection<br>• Before transmitting a device checks if the channel is busy<br>• If it is busy the device waits // if channel free data is sent<br>• When transmission begins the device listens for other devices also beginning transmission<br>• If there is a collision, transmission is aborted / transmitting a jam signal<br>• Both devices wait a (different) random time, then try again | **4** |

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| Question | Answer | Marks |
|---|---|---|
| 4(a) | **1 mark** for 3 or 4 correct products<br>**2 marks** for all 5 correct products<br><br>$X = \bar{A}.\bar{B}.\bar{C} + \bar{A}.\bar{B}.C + \bar{A}.B.C + A.\bar{B}.C + A.B.C$ | **2** |
| 4(b) | **1 mark** for correct answer<br><br>AB<br>C<br><br>\| | 00 | 01 | 11 | 10 \|<br>\| 0 \| 1 \| 0 \| 0 \| 0 \|<br>\| 1 \| 1 \| 1 \| 1 \| 1 \| | **1** |
| 4(c) | 1 mark per correct loop<br><br>AB<br>C<br><br>\| | 00 | 01 | 11 | 10 \|<br>\| 0 \| 1 \| 0 \| 0 \| 0 \|<br>\| 1 \| 1 \| 1 \| 1 \| 1 \| | **2** |
| 4(d) | **1 mark** per bullet point.<br><br>• $\bar{A}.\bar{B}$<br>• $+C$<br><br>$X = \bar{A}.\bar{B} + C$ // $X = C + \bar{A}.\bar{B}$ | **2** |

| Question | Answer | Marks |
|---|---|---|
| 5(a) | **1 mark** per bullet point to **max 2**<br><br>• No suitable data type is provided by the language used<br>• The programmer needs specify a new data type<br>• … that meets the requirements of the application / program | **2** |
| 5(b)(i) | **1 mark** per bullet point<br><br>• `EmployeeID` declared as `STRING`<br>• `Sales, Technical` and `CustomerServices` …<br>• … with commas in-between<br>• `ENDTYPE`<br><br>`TYPE Employee`<br>`  DECLARE EmployeeID : STRING`<br>`  DECLARE EmployeeName : STRING`<br>`  DECLARE Department : (Sales, Technical, CustomerServices)`<br>`  DECLARE Salary : 25000..150000`<br>`ENDTYPE` | **4** |
| 5(b)(ii) | `DECLARE NewEmployee : Employee` | **1** |
| 5(b)(iii) | `NewEmployee.EmployeeID ← "02244"` | **1** |
| 5(b)(iv) | **1 mark** per bullet point to **max 2**<br><br>• Array<br>• List<br>• Set<br>• Collection<br>• Class<br>• Stack<br>• Queue<br>• Linked list<br>• Dictionary | **2** |
| 6(a) | **1 mark** per bullet point<br><br>Page:<br>• Virtual Memory is divided into blocks of a fixed size<br><br>Page frame:<br>• the main memory is divided into page frames of the same size as a page<br><br>Page table:<br>• the Page (Map) table shows the mapping of pages to page frames | **3** |

| Question | Answer | Marks |
|---|---|---|
| 6(b) | **1 mark** per bullet point to **max 3**<br><br>• To allow multiprogramming / multitasking to take place<br>• To ensure fair usage of the processor<br>• To ensure fair usage of peripherals<br>• To ensure fair usage of memory<br>• To ensure higher priority tasks are executed sooner<br>• To ensure all processes have the opportunity to finish | **3** |
| 6(c) | A **signal** from a software **source** or hardware device seeking the **attention of the processer** | **1** |
| 6(d) | **1 mark** per bullet point in the order given<br><br>• JOB32<br>• JOB42<br>• JOB42 | **3** |

| Question | Answer | Marks |
|---|---|---|
| 7(a) | **1 mark** per bullet point<br><br>• Application<br>• Transport<br>• Internet / Network<br>• Data Link | **4** |
| 7(b)(i) | **1 mark** per bullet point to **max 2**<br><br>• Packet switching makes best use of the available (channel) capacity<br>• … by using alternative routes<br>• … which is more secure / robust<br>• … as packets to / from different sources and destinations can share the same route | **2** |
| 7(b)(ii) | **1 mark** per bullet point to **max 2**<br><br>• To store data about packet<br>• … and its routing // to ensure it reaches its destination<br>• ... to ensure that message can be properly reconstructed | **2** |

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| Question | Answer | Marks |
|---|---|---|
| 7(b)(ii) | **1 mark** per item to **max 3**<br><br>For example:<br>• IP address of sender<br>• IP address of destination<br>• IP version<br>• Number of packets the message consists of<br>• ID number of that packet<br>• Protocol used<br>• Packet length<br>• Time to live // max number of hops<br>• Synchronisation data<br>• Source port<br>• Destination Port<br>• Checksum | **3** |

| Question | Answer | Marks |
|---|---|---|
| 8(a) | **1 mark** per bullet point to **max 2**<br><br>• Serial number<br>• Identification of Certificate Authority (that issued the certificate)<br>• Version (number)<br>• Valid from // start date<br>• Valid to // end date<br>• Subject name (name of user/owner/computer/network device)<br>• Subject's public key<br>• Hashing algorithm<br>• Algorithm used to create signature<br>• Algorithm used to hash certificate<br>• Hashed certificate | **2** |
| 8(b) | **1 mark** for each correct term<br><br>A **hashing** algorithm is used to generate a message digest from the plain text message. The message digest is **encrypted** with the sender's **private key**. | **3** |

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<table>
<tr><th>Question</th><th>Answer</th><th>Marks</th></tr>
<tr><td>9(a)</td><td>1 mark for each correct term<br><br>
<table>
<tr><th>Description</th><th>Term</th></tr>
<tr><td>• There are several processors.<br>• Each processor executes different sets of instructions on one set of data at the same time.</td><td>MISD</td></tr>
<tr><td>• The processor has several ALUs.<br>• Each ALU executes the same set of instructions on different sets of data at the same time.</td><td>SIMD</td></tr>
<tr><td>• There is only one processor.<br>• The processor executes one set of instructions on one set of data.</td><td>SISD</td></tr>
<tr><td>• There are several processors.<br>• Each processor executes a different set of instructions.<br>• Each processor operates on different sets of data.</td><td>MIMD</td></tr>
</table></td><td>4</td></tr>
<tr><td>9(b)</td><td>1 mark per bullet point to max 3<br><br>• A large number of processors<br>• Collaborative processing // coordinated simultaneous processing<br>• Network infrastructure<br>• Communicate using a message interface / by sending messages</td><td>3</td></tr>
</table>

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